

# No more LRU, Simple Scalable **Caching with only FIFO Queues**





#### **Juncheng Yang Harvard University**







### Software cache

- Software cache: all decisions made in software





# • Cache: A fast but small storage storing a portion of the dataset to speed up data access





# Software cache is deployed everywhere

Ubiquitous deployment

- speed up data access
- reduce data movement
- reduce repeated computation











$\leftarrow \rightarrow$	C	ඛ		twitte	er.com	ן ו	Ł							$\supset$	◙		G	60	•	<u>ع</u>	}	⊻	G	۵	) (	3	
About	Store					L	~												Gi	mail	Imag	jes	盗			(	
									-	Ç	<mark>,Ç</mark>		<u>)(</u>		ę.	2											
					۹													ş	ļ,	:							
									Goog	gle S	earch		l'm	Feelin	g Lucl	ky											
					-	► We	lcome	to the	Gemi	ni era	a: Unioc	cking	new w	vays to	help y	ou bui	d and	scale	with	AI							
	Ele	emen	ts	Cons	ole	So	urces	1	Vetwo	ork	Per	form	ance	N	lemor	у	Appli	catio	n 2	»>			<u>¤</u> 9	{	<b>;</b> ;	:	
◙ ⊘		۹		Pres	erve lo	og	🗹 D	isabl	e cac	he	No th	rottli	ng	• 3	(i\$	<u>1</u> :	₽.										٤
Filter					🗌 Inv	/ert		Hide	data	URL	_s 🗌	🛛 Hi	de ex	tensic	on UR	Ls											
All	) JS	Fe	tch/XI	HR	css	Fon	t In	ng	Media	1) (M	lanife	st	ws	Wasr	n (O	ther		Block	ed r	espo	nse	cook	es				
Block	ed requ	lests		3rd-p	arty r	eque	sts																				

Recording network activity...

Perform a request or hit **# R** to record the reload.

Learn more

# HOW many requests are served by caches?



# A typical web application



cache





# A typical web application









#### Software cache consumes a huge amount of resources

- Google CliqueMap: PBs of DRAM<sup>[1]</sup>
- Twitter: 100s clusters, 100s TB of DRAM, 100,000s cores<sup>[2]</sup>
- Meta, Pinterest...
- How to make caching more sustainable?
- Less DRAM: caching more useful objects {DDDD}{DDDD}{DDDD}{DDDD}

• Fewer CPU cores: faster and concurrent cache hits 

[1] CliqueMap: Productionizing an RMA-Based Distributed Caching System, SIGCOMM'21 [2] A large scale analysis of hundreds of in-memory cache clusters at Twitter, OSDI'20



10000

#### efficient caching

fast and scalable caching



# The core of a cache

# eviction algorithm

#### The need for simple and scalable cache eviction algorithm

- - not scalable
  - increasingly complex

CLOCK	SLRU	2Q	
LRU	LRU-K	EELRU	
1960 1980			2

"Predicting which pages will be accessed in the near future is tricky, and the kernel has evolved a number of mechanisms to improve its chances of guessing right. But the kernel not only often gets it wrong, it also spend a lot of CPU time to make the incorrect decision."

60+ years of research on designing eviction algorithms: most are LRU-based

HALP UBM TinyLFU MQ LeCaR LRB LRFU ARC CACHEUS LHD ACME LIRS 000 2020

— kernel developer





# A simple algorithm: FIFO eviction algorithm

- First-in-first-out (FIFO)
  - simpler than LRU
  - fewer metadata
  - no computation
  - more scalable
  - flash-friendly



#### The only drawback: FIFO has a high miss ratio







## **Existing eviction algorithms focus on promotion**



#### traditionally





### **Demotion should be the first-class citizen**







#### Benefits of lazy promotion

- less computation
- better decision
- more efficient (lower miss ratio)





# Lazy promotion: only promote during eviction



Why is FIFO-Reinsertion more efficient? evict new objects faster!



Objects push N towards eviction									
LRU	А	Х							
<b>FIFO-Reinsertion</b>	А	Х	В						



# **Quick demotion: quickly evict new objects**

- One-hit wonders: objects appeared once in the sequence
- Cache workloads: shorter request sequences have larger one-hit-wonder ratios







# **Quick demotion: quickly evict new objects**

- One-hit wonders: objects appeared once in the sequence
- Cache workloads: shorter request sequences have larger one-hit-wonder ratios

#### One-hit-wonder ratio of week-long traces at 10% length: **72%** (mean on **6594** traces)

Implication: most objects are not used before evicted





17

#### Observation

#### Most objects are not reused before evicted



LRU cache running MSR workload



LRU cache running Twitter workload



18

## Quick demotion: quickly evict new objects

#### **ARC: A SELF-TUNING, LOW OVERHEAD REPLACEMENT CACHE**

Nimrod Megiddo and Dharmendra S. Modha IBM Almaden Research Center, 650 Harry Road, San Jose, CA 95120

four LRU queues

+ adaptive algorithm

Abstract in a deman propose a Replacemen In respo

substantial the entire

nory levels:

dynamically, adaptively, a recency and frequency components in an online and selftuning fashion. The policy ARC uses a learning rule to adaptively and continually revise its assumptions about the workload.

The policy ARC is *empirically universal*, that is, it empir-

assumed to be significantly faster than the auxiliary memory, but is also significantly more expensive. Hence, the size of the cache memory is usually only a fraction of the size of the auxiliary memory. Both memories are

#### **TinyLFU: A Highly Efficient Cache Admission Policy**

#### three LRU queues + a new metric

#### ABSTR

Although LRU replacement policy has in the buffer cache management, it is well known for its inability to cope with access patterns with weak locality. Previous work, such as LRU-K and 2Q, attempts to enhance LRU capacity by making use of additional history information of previous block references other than only the recency information used in LRU. These algorithms greatly

#### LHD: Improving Cache Hit Rate by Maximizing Hit Density

Nathan Reckman

# The secret sauce of state-of-the-art algorithms: evicting new objects very aggressive

LIRS: An Efficient Low Inter-reference Recency Set **Replacement Policy to Improve Buffer Cache Performance** 

> **1.1 The Problems of LRU Replacement Policy** The effectiveness of cache block replacement algorithms is critical to the performance stability of I/O systems. The LRU (Least Recently Used) replacement is widely used to manage buffer cache due to its simplicity, but many anomalous behaviors have been found with some typical workloads, where the hit rates of LRU may only slightly increase with

Haoxian Chei

Asaf Cidor

#### **Cliffhanger: Scaling Performance Cliffs in Web Memory Caches**

Asaf Cidon<sup>1</sup>, Assaf Eisenman<sup>1</sup>, Mohammad Alizadeh<sup>2</sup>, and Sachin Katti<sup>1</sup>

#### LRU + partitioning

put and reduce user latency. Small performance improv ments in these systems can result in large end-to-end gains. For example, a marginal increase in hit rate of 1% can reduce the application layer latency by over 35%. However, existing web cache resource allocation policies are workload oblivious and first-come-first-serve. By an

AB

cach

hit rate by just 1% would reduce the read latency by over 35% (from  $376\mu s$  at 98.2% hit rate to  $278\mu s$  at 99.2% hit rate). The end-to-end speedup is even greater for user queries, which often wait on hundreds of reads [26]. Web caching systems are generally simple: they have

HALP: Heuristic Aided Learned Preference Eviction Policy for YouTube Content Delivery Network



# **Simple, Scalable caching with <u>three Static FIFO</u> queues**



https://s3fifo.com

#### **S3-FIFO design QUICK DEMOTION** on *cache miss* 2 if not in ghost, else struct object { ... uint8 t cnt:2; small 3 on *eviction* on cache hit if cnt <= 1, cnt++ ghost





### **S3-FIFO features**

- Simple and robust: static queues
- **Fast:** no metadata update for most requests
- Scalable: no lock
- Tiny metadata: 2 bits
- Flash-friendly: sequential writes

#### Can be implemented using one, two or three FIFO queues





### **Evaluation setup**

- Data lacksquare
  - 14 datasets, 6594 traces from Twitter, Meta, Microsoft, Wikimedia, Tencent, Alibaba, major CDNs...
  - 848 billion requests, 60 billion objects
  - collected between 2007 and 2023
  - block, key-value, object caches
- Platform
  - libCacheSim, cachelib
  - PDL cluster and CloudLab with 1 million core-hours
- Metric lacksquare
  - miss ratio reduction from FIFO
  - throughput in Mops/sec

#### Data and software are open-sourced



# 





### **S3-FIFO is efficient across datasets**



Wikimedia CDN Meta CDN 🕂 TencentPhoto CDN Twitter KV Meta KV Social Network KV Alibaba (block) Tencent (block) Systor (block) CloudPhysics (block) MSR (block) fiu (block)

Evaluated on 6594 traces with 848 billion requests from 12 sources, collected between 2007 and 2023 This evaluation is million times larger than previous works





# Throughput scales with number of threads



Implemented in Meta CacheLib, synthetic Zipf 1.0 workload, benchmarked on Cloudlab r6420 node



### Recap

- S3-FIFO: simple, scalable caching with three static FIFO queues
  - prevalence of one-hit wonders
  - small FIFO queue: quickly evict one-hit wonders, reinsertion: keep popular objects
- The first work showing that FIFO queue is sufficient to design efficient algorithms
- S3-FIFO recognition and impact
  - Covered by many blogs, newsletters, meetups in English, Chinese, Korean, Japanese...
  - Deployed at Google, VMware, Redpanda...
  - More than ten open-source libraries implemented S3-FIFO in Rust, C, C++, JavaScript, Python...









### **SIEVE** An eviction algorithm simpler than LRU



https://sieve-cache.com

### The secret to designing efficient eviction algorithms



### LAZY PROMOTION

Retain popular objects with minimal effort



#### QUICK DEMOTION Beneve unpequier objects fact

Remove unpopular objects fast, such as one-hit-wonders



#### SIEVE: combining lazy promotion and quick demotion **A small change turn FIFO-Reinsertion to SIEVE**







### **SIEVE features**

- Extremely simple
- ZERO parameter
- Fast and scalable
- Small per-object metadata
- TTL-friendly



### Why does SIEVE work?

• Retain popular objects effectively

• Evict new objects quickly

Separate new and old objects







#### SIEVE also achieves the lowest miss ratio on the well-studied Zipfian workloads



### SIEVE achieves the lowest byte miss ratio



Better than all state-of-the-art algorithms



Small cache: better than ARC, close to LRB Large cache: better than LRB



# SIEVE throughput scales with the number of threads



compared to optimized LRU: 16% faster with a single thread, 2x faster with 16 threads



# **SIEVE is simple to implement**

Cache library	Language	Eviction algorithm	Lines of change
groupcache	Golang	LRU	21
mnemonist	Javascript	LRU	12
lru-rs	Rust	LRU	16
Iru-dict	Python + C	LRU	21



#### Adoption Large systems: 🔊 Pelikan 🗿 Nyrkiö 姉 SkiftOS 😁 DragonFly DNSCrypt-proxy encrypted-dns-resolver Cache libraries: 👔 golang-fifo 🏟 js-sieve 🏈 rust-sieve-cache 🐲 go-sieve sieve\_cache (Ruby) a zig-sieve (Zig) sieve (Swift) 🔊 sieve (JavaScript) 📅 sieve (Elixir) 😭 sieve (Nim) sieve-cache (Java) 🚳 sieve (Python) 📳 sieve-cache-in-rust sieve-cache (JavaScript) gosieve, sieve (typescrpt)



### SIEVE can be used as a cache primitive

#### LeCaR: LRU + LFU + ML TwoQ: LRU + FIFO ARC: LRU + LRU + 2 ghost queues





### SIEVE can be used as a cache primitive

# LeCaR: LRU + LFU + ML TwoQ: LRU + FIFO ARC: LRU + LRU + 2 ghost queues

**Replace LRU with SIEVE** 





## SIEVE has been widely used

- SIEVE is available in 20+ cache libraries with 12 programming languages
- Production systems integrated SIEVE: Pelican, SkiftOS, DragonFly...



#### ries with **12** programming languages E: Pelican, SkiftOS, DragonFly...

...



Richard Artoul 🤣 @richardartoul

Turns out Ristretto cache is \*async\*... I switched WarpStream's footer cache from Ristretto to golang-fifo (Sieve algo) and got a 33x reduction in cache misses and 16% CPU savings...

C						
e	Richard Artoul Updated 4 minutes ago					
-				🖉 Edi	it RA	1
S	um:warpstream_loading_cache_loader_outcome{service:w 🗇	~				
	400					
r second	200	$\wedge$				
Pe	0			-		
	20:08 20:09 20:10 20:11 20:12 20:13 20:14 20:15 20:16	20:17 20:1	8 20:19	20:20	20:21	20:22
	lags in sum:warpstream_loading_cacne_loader_outcome(service:warp-agent,env	6e-3 /s	0 /s	0.10 /s	0.20 /s	value
	cache_name.acis/nosci occasis/iorocoaroc/		0 /s	0.30 /s	0.50 /s	-
	cache_name:acls,host:i-0ddd25eb52bf97b6f	0.01 /s	015			
	cache_name:acls,host:i-0ddd25eb52bf97b6f cache_name:acls,host:i-0ee0b6128679455ed	0.01 /s 0.013 /s	0 /s	0.40 /s	0.40 /s	

9:35 PM · Jan 20, 2024 · 17.3K Views



# Summary

- Two techniques
  - Lazy promotion
  - Quick demotion
- S3-FIFO: simple, scalable caching with three static FIFO queues
  - small queue for filtering
  - main queue with reinsertion
- SIEVE: the simplest algorithm combining lazy promotion and quick demotion

#### https://s3fifo.com



#### https://sieve-cache.com





# How we can build better storage systems together

#### workload sharing

Juncheng Yang https://junchengyang.com



